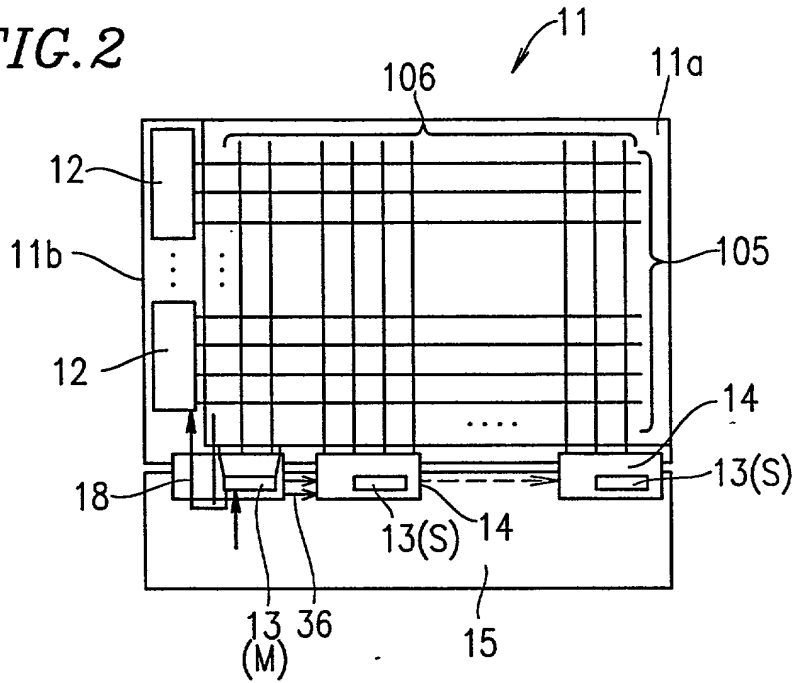


*FIG. 2*



*FIG. 3A*

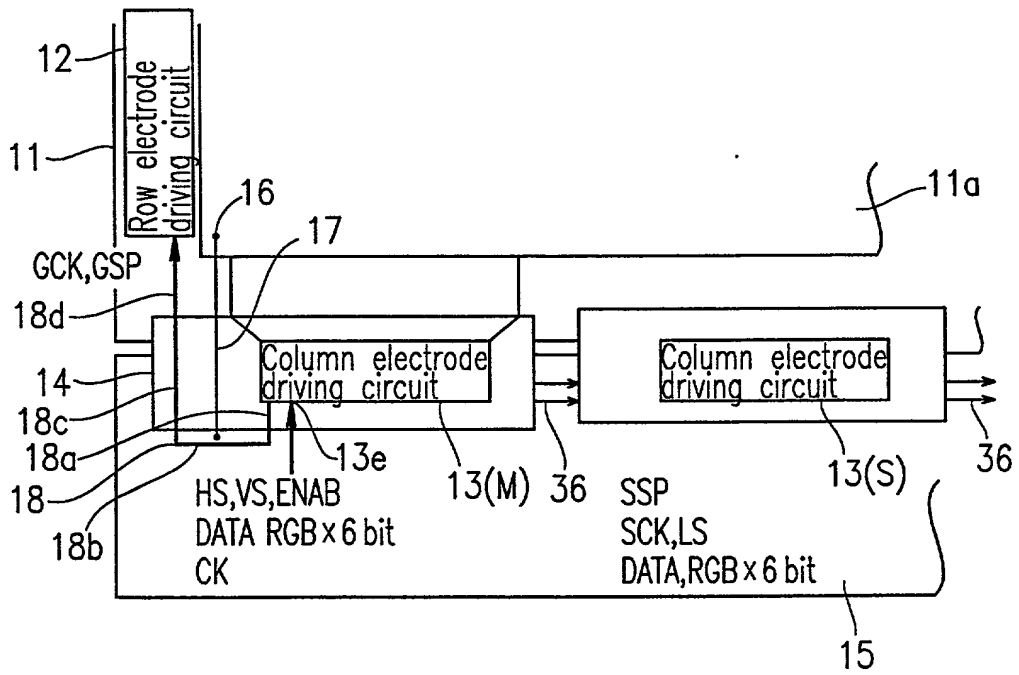


FIG. 3B

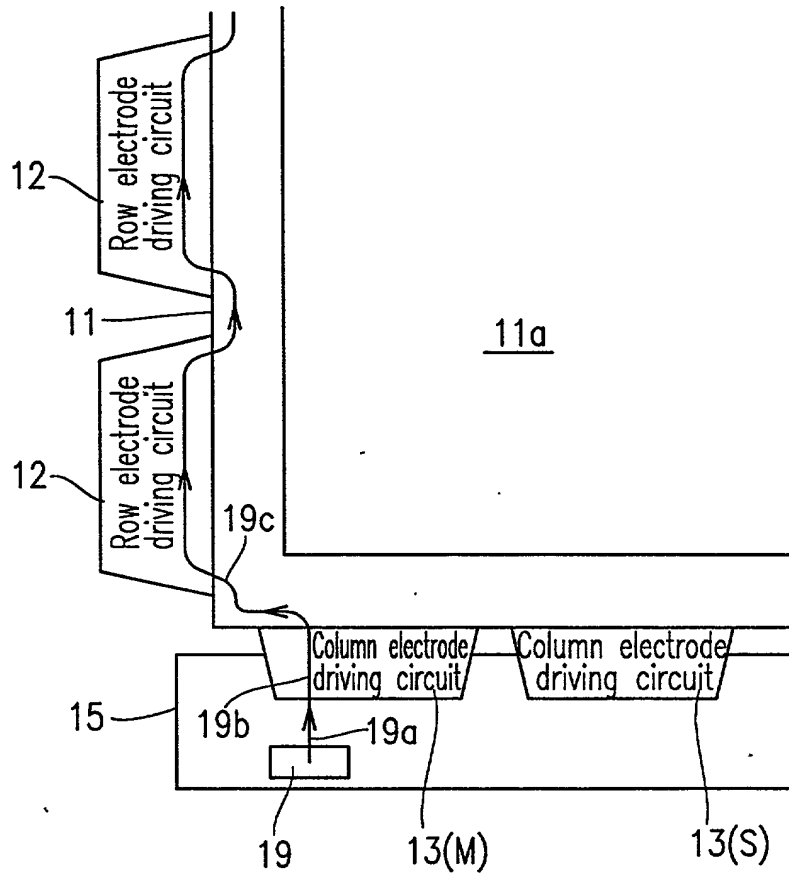


FIG. 4A

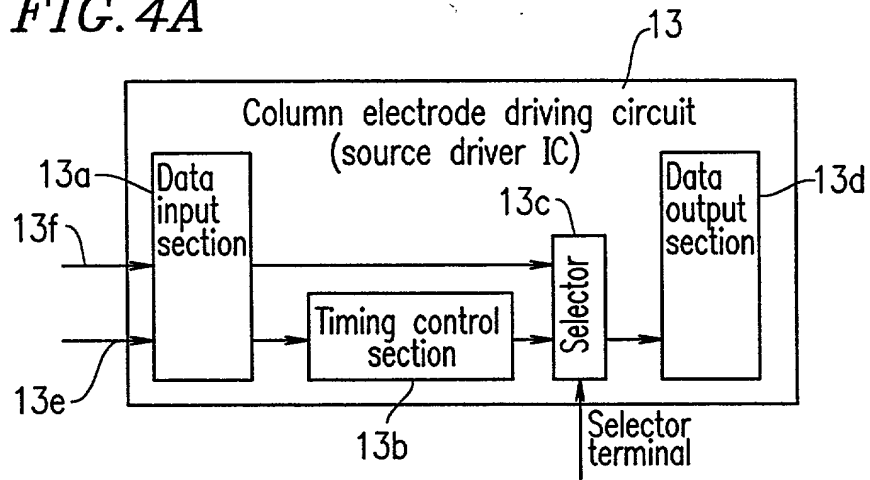


FIG. 4B

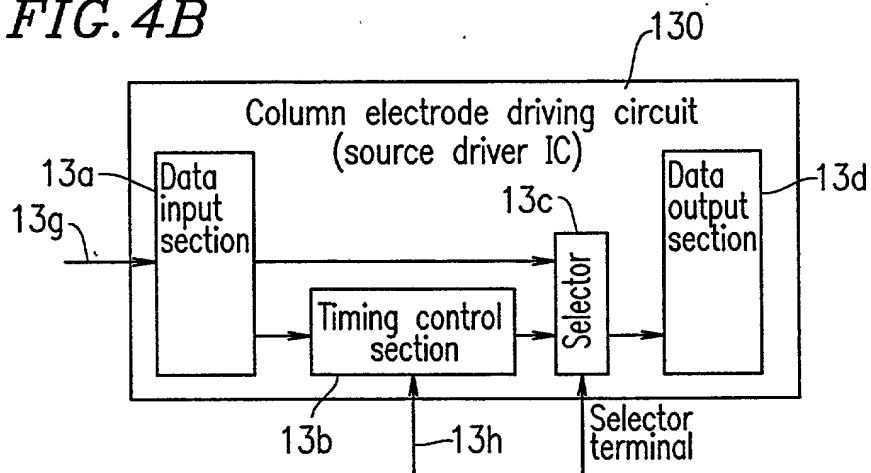


FIG. 5 is a block diagram of a display device 21. The device includes a display panel 22 with a grid of pixels 23. A row electrode driving circuit 29a and column electrode driving circuit 25 are connected to the panel. A timing controller IC 31 is connected to the driving circuits and receives RGBx3 DATA, HS, VS, ENAB, and CK signals. The panel is also connected to a power supply 33 and a ground 34.

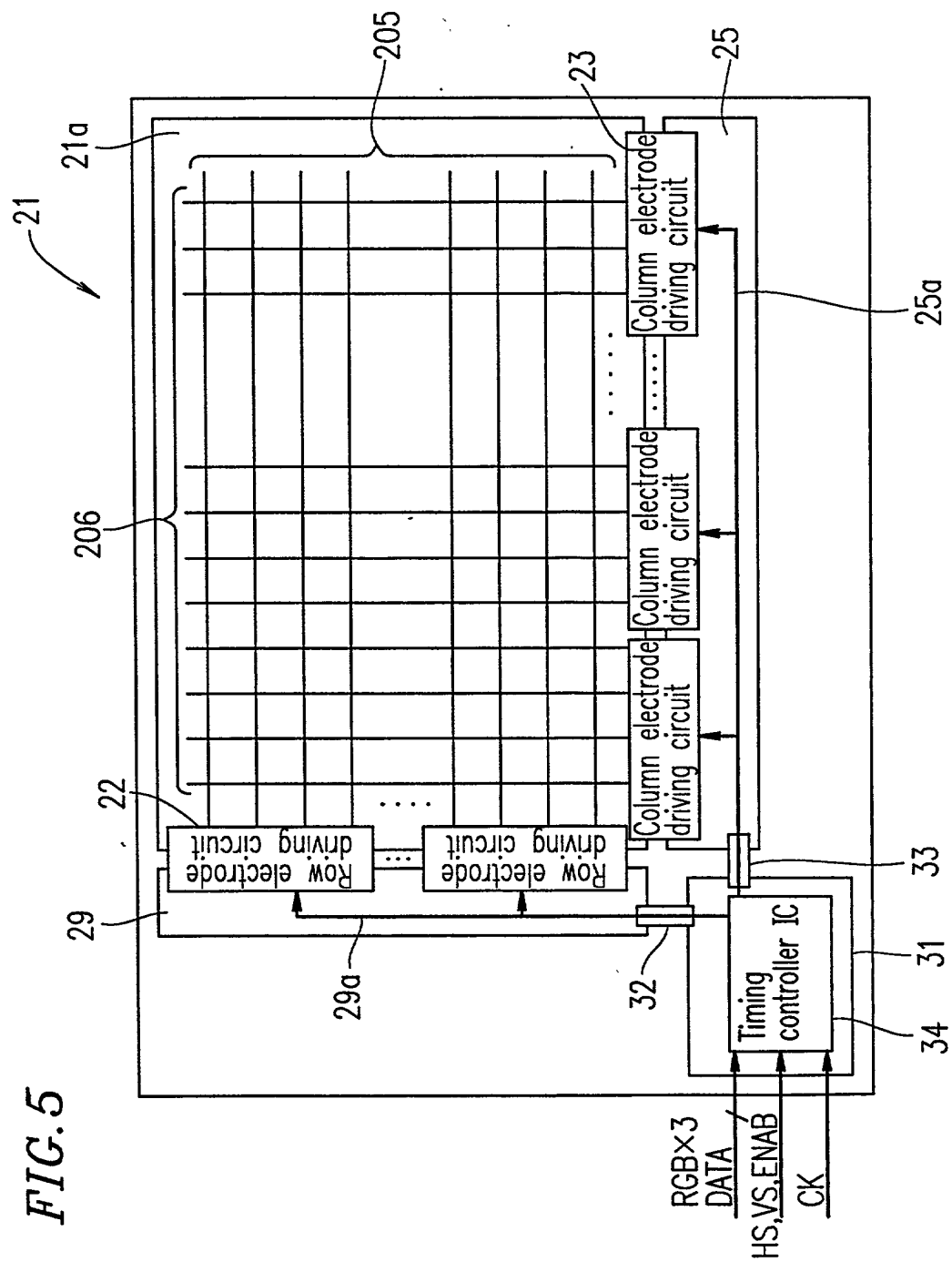


FIG. 6

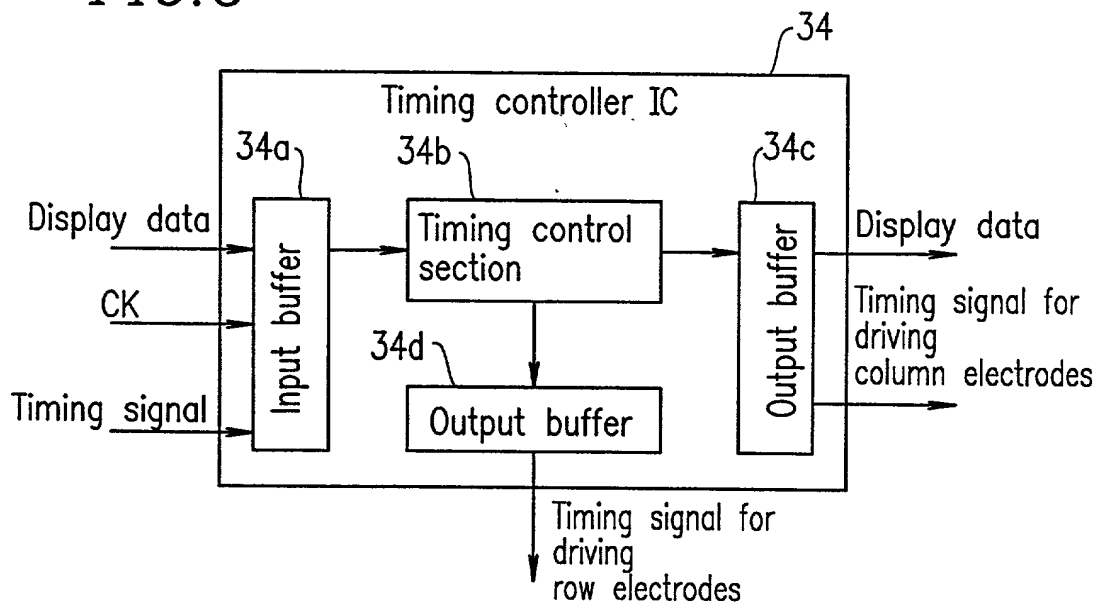


FIG. 7

